

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|---|------------------|---------|------------------|
| L1 | 0 | instruction with decoder with register with acs with branch with metric with accumulator\$1 with viterbi with turbo | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/24 15:22 |
| L2 | 0 | decoder with register with acs with branch with metric with accumulator\$1 with viterbi with turbo | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/24 15:22 |
| L3 | 0 | decoder with register with acs with branch with metric with accumulators with viterbi with turbo | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/24 15:22 |
| L4 | 0 | decoder with acs with branch with metric with accumulators with viterbi with turbo | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/24 15:23 |
| L5 | 0 | decoder with branch with metric with accumulators with viterbi with turbo | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/24 15:23 |
| L6 | 4 | decoder with branch with metric with viterbi with turbo | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/10/24 15:23 |

Interference Search
SWR
10/24/06